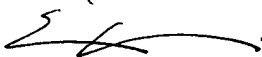


REMARKS

The specification has been amended to correct the descriptions of Figures 2-4, 7, 8, and 10-12 in the Brief Descriptions of the Drawings.

Examination on the merits is requested.

Respectfully submitted,



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VERSION WITH MARKINGS TO SHOW CHANGES MADE

[Fig. 2 is a cross section view] Figs. 2A, 2B, and 2C are cross sectional views of a TFT according to the example 1;

[Fig. 3 is a cross section view] Figs. 3A, 3B, and 3C are cross sectional views of a TFT according to the example 1;

[Fig. 4 is a cross section view] Figs. 4A and 4B are cross sectional views of a TFT according to the example 1;

[Fig. 7 is a top view] Figs. 7A, and 7B are top views and a cross section view of an EL display panel according to the example 3;

[Fig. 8 is a cross section view] Figs. 8A and 8B are cross sectional views of an EL display panel according to the example 3;

[Fig. 10 is a view] Figs. 10A, 10B, 10C, 10D, 10E and 10F are views showing a variety of semiconductor devices according to the example 4;

[Fig. 11 is a view] Figs. 11A, 11B, 11C and 11D are views showing a variety of semiconductor devices according to the example 4; and

[Fig. 12 is a view] Figs. 12A, 12B and 12C are views showing a variety of semiconductor devices according to the example 4.

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